In The Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

1. (Currently amended) A signal processing system comprising:

means for receiving an incoming radio frequency signal;

means for narrowing the received incoming signal to a limited frequency band;

means for amplifying the narrowed incoming signal;

means for rejecting an image of the narrowed incoming signal to output an input signal;

~~first~~ means for distributing ~~an~~ the input signal ~~between two or more~~ to one of two or more channels ~~in a current mode of operation~~;

~~second~~ means disposed in each of said channels for processing ~~said input signal~~ the distributed signal and providing an output signal in response thereto, wherein only one of said processing means is active at a time; and

~~third~~ means for combining the signals output by two or more said processing means~~; and~~

~~fourth means for controlling said first and said third means~~.

2. (canceled).

3. (Currently amended) The invention of Claim 1 wherein said ~~first~~ distribution means includes a mixing circuit.

4. (Currently amended) The invention of Claim 3 wherein said mixing circuit further includes means for providing automatic gain control for each of said channels ~~individually~~.

5. (Original) The invention of Claim 4 wherein said means for providing automatic gain control operates in a current mode.

6. (Currently amended) The invention of Claim ~~5~~ 4 wherein said means for providing automatic gain control includes a digital automatic gain control circuit.

7. (Currently amended) The invention of Claim ~~6~~ 3, wherein said mixing circuit further ~~including~~ includes means for selectively providing differential digital automatic gain control signals in response to a channel select signal.

8. (Original) The invention of Claim 3 wherein said mixing circuit further includes means for mixing said input signal with a mixing signal.

9. (Original) The invention of Claim 8 wherein said mixing circuit operates in a current mode.

10. (canceled).

11. (Currently amended) The invention of Claim ~~10~~ 3 wherein said mixing circuit includes at least one Gilbert cell.

12. (Currently amended) The invention of Claim ~~11~~ 3 wherein said mixing circuit includes a transconductance amplifier.

13. (Original) The invention of Claim 12 wherein said mixing circuit includes an automatic gain control circuit.

14. (canceled).

15. (Currently amended) A receiver comprising:

a radio frequency stage for downconverting a received signal and providing said input signal in response thereto;

~~first means~~ a distributor for distributing said input signal ~~between two or more~~ to one of two or more channels ~~in a current mode of operation~~, said ~~first means~~ distributor including a mixing circuit having:

a Gilbert cell for each channel,

an automatic gain control circuit for each channel in communication with a respective one of said Gilbert cells, and

a transconductance amplifier in communication with said automatic gain control circuits;

~~second means disposed in each of said channels for processing said input signal and providing an output signal in response thereto, second means including first and second filters disposed in a first and a second channel respectively~~

a filter disposed in each of said channel for processing said distributed signals and outputting the processed signals; and

~~third means~~ a combining circuit for combining the signals output by said processing means~~;~~ ~~and~~

~~fourth means for controlling said first and said third means~~.

16. (Currently amended) A signal processing method comprising the steps of:

receiving an incoming signal;

downconverting the incoming signal;

rejecting images within the incoming signal to thereby output input signal;

distributing ~~an~~ said input signal ~~between~~ to one of two or more channels in a current mode of operation;

processing ~~said input signal~~ the distributed signals and providing ~~an output signal~~ output signals in response thereto, wherein the distributed signals are processed one at a time; and

combining the output signals ~~output by said processing means; and~~

~~controlling said first and said third means~~.

17. (New) A signal processing circuit comprising:

a receiver for receiving an incoming signal;

a pre select filter connected to the receiver for filtering the received incoming signal;

a low noise amplifier connected to the pre select filter for amplifying the filtered incoming signal;

a image rejection filter connected to the low noise amplifier for rejecting predetermined images of the amplified incoming signal to thereby output an incoming signal;

a distributor connected to the image rejection filter for distributing the input signal to one of at least two channels in a current mode of operation;

an intermediate-frequency filter disposed in each of said two channels for processing said input signal and providing an output signal in response thereto, wherein only one of said intermediate-frequency filters is active at a time; and

a mixer connected to the outputs of each intermediate-frequency filter for combining the signals output by each of said intermediate-frequency filter.

18. (New) A receiver comprising:

a radio frequency stage for downconverting a received signal and providing said input signal in response thereto;

a distributor for distributing said input signal to one of at least two channels in a current mode of operation, said distributor including a mixing circuit having:

a Gilbert cell for each channel,

an automatic gain control circuit for each channel operatively coupled with a respective one of said Gilbert cells, and

a transconductance amplifier operatively coupled with said automatic gain control circuits; and

a filters disposed in each of said channels for processing said input signal and providing an output signal in response thereto.